

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 63-188276
 (43)Date of publication of application : 03.08.1988

(51)Int. CI. G06F 15/66
 G09G 1/16
 H04N 1/393

(21)Application number : 62-020847 (71)Applicant : MATSUSHITA ELECTRIC IND CO LTD
 (22)Date of filing : 30.01.1987 (72)Inventor : KODA TOSHIYUKI
 SHIMEKI TAIJI
 TATSUMI TOSHIICHI
 KAWAHARA TOSHIYUKI

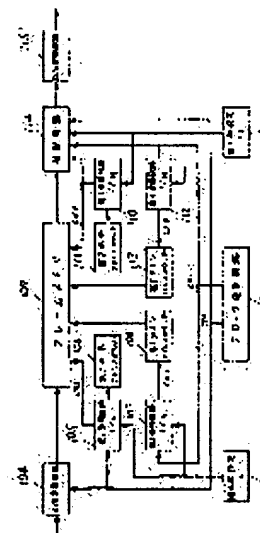
(54) PICTURE PROCESSING DEVICE

BEST AVAILABLE COPY

(57)Abstract:

PURPOSE: To prevent an output picture from being changed in a mosaic shape and to obtain a smoothly magnified or reduced picture, by providing a circuit which interpolates a data missed at the time of magnifying an timage according to a magnification rate.

CONSTITUTION: When the magnification rates M are applied on frequency demultipling circuits 110 and 112 by a magnification rate setting register 102, a reference dot clock CK0 and a reference line clock CK1 are frequency-demultiplied by 1/M respectively and a readout dot clock CK4 and a readout line clock CK5 are generated. By taking out a data from a frame memory 109 based on a readout clock signal, and interpolating 114 a value between the data in a horizontal and a vertical directions, then, D/A converting 115, the data in the frame memory 109 can be magnified by M times. In such a way, it is possible to prevent the output picture in a unit of block of $M \times M$ picture elements from being changed in the mosaic shape, and to obtain the smoothly magnified and reduced picture.



LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision
of rejection]

[Date of requesting appeal against examiner's
decision of rejection]

[Date of extinction of right]

Copyright (C); 1998, 2003 Japan Patent Office